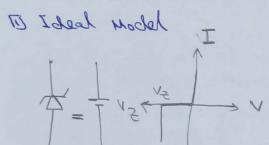
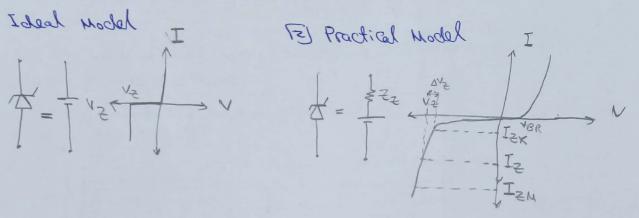
=> The Zener Diode

The Zenle is a silicon PM Junction that is designed to offere to in the severse breakdown region - when the Zener reaches reverse breakdown its voltage remains constant even though the Current changes drastically.

@ Zeres equivalent circuits





-> Zz = AVZ

> IZK is the minimum knasse current required to keep the Zener in breakdown for voltage regulation

> IZM is the Maximum HISTER CUERN'T about it the Berer May be demaged.

(note that) I < I < I < I < M to keep the rewest voltage a cross the Zener Constant.

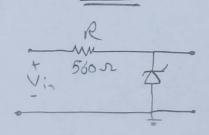
-> The Zenes wittage Change with the Change in temperature according to DVZ=VZ* TC*AT when TC is Per Centrage AVZ=TC * AT Where TC is in MV

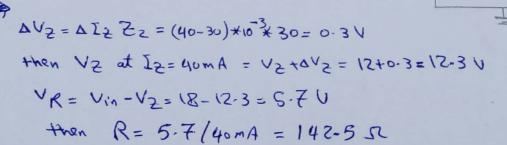
Izm = Po (Max)

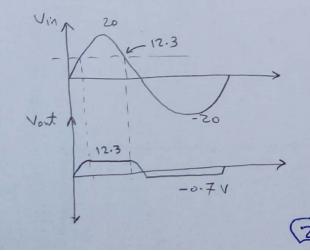
@ Zever Diode Application Line regulation = A Vost 1- Zerer regulation with a variable input voltage. 2- Zeres regulation with available load. Load regulation = VNL-VEL 3 - Zener limiter

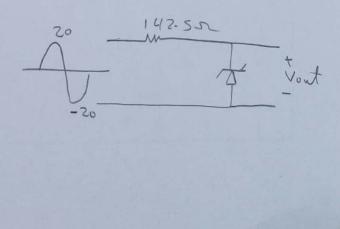
Sheet #5

$$Z_z = \frac{\Delta V_z}{\Delta Iz} = \frac{V_{zz} - 4.7}{(50 - 25) \times 10^{-3}} = 15 \Rightarrow V_{zz} = 6-075 \text{ V}$$





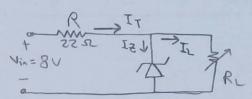




5) given Vz=5.1 u at Iz = 49 mA, Izk = 1mA, Izk = 70mA and Z=7A

Find IL(min), IL(max)

A Maximum Load Current-VZ(min) = VZ - AIZ ZZ



VZ(min) = 5.1 - (49-1) *10-3 * 7 = 4.764 V

@ Minimum Logal Current

$$V_{2(Mqx)} = V_{2} + \Delta I_{2} Z_{2} = 5.1 + (70-49) \times 10^{-3} \times 7 = 5.24 TV$$

(#) Example: Determine the output vollage waveform for each circuit

